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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Atsushi SANO et al.

Group Art Unit: 2812

Application No.: 10/574,571

Examiner: S. NIKMANESH

Filed: April 28, 2006

Docket No.: 127570

For: MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND SUBSTRATE
PROCESSING APPARATUS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. One or more reference cited herein was cited in a counterpart foreign application. An English language version of the foreign Office Action is attached for the Examiner's information. See Reference 1.
- ☒ 3. An English language Abstract of one or more non-English language reference is attached. See Reference 1.

- ☒ 4. A computer-generated English language translation of one or more Japanese Patent Publication cited herein has been obtained from the website of the Japanese Patent Office ([<http://www.jpo.go.jp>]), and is attached, but has not been reviewed for accuracy. See Reference 1.
- ☒ 5. JP-A-2002-343790 and JP-A-2005-45166, cited in the Office Action, were previously submitted to the Patent Office in the June 3, 2008 Information Disclosure Statement.

Respectfully submitted,


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Date: July 16, 2008

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